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power save bit threshold

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 JT Kao, AP Chandrakasan - Solid-State Circuits, IEEE Journal of, 2000 - [ieeexplore.ieee.org](#)

... less glitching is attractive from a low-power point of ... LSI logic and was applied to a -bit Wallace tree ... Initially, the AND gates and the carry-save adder units ...

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[Thermal management system for high performance PowerPC TM microprocessors](#)

 H Sanchez, B Kuttanna, T Olson, M Alexander, G ... - Compton'97. Proceedings, IEEE, 1997 - [ieeexplore.ieee.org](#)

... system runs the TAU in single threshold mode ... disabled by setting the interrupt enable bit (TIE) to zero ... decrease comes from the dynamic power management of each ...

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[Transistor sizing issues and tool for multi-threshold CMOS technology -](#)

 ►[dzineworkstudio.com](#) (PDF)

 J Kao, A Chandrakasan, D Antoniadis - Annual ACM IEEE Design Automation Conference: Proceedings of ..., 1997 - [doi.ieeecomputersociety.org](#)

... Also, since SOI is emerging as a likely candi- date for low power circuit design, and SOI ... A larger MTCMOS circuit like an 8x8 bit carry save multi- plier ...

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[Particle-induced bit errors in high performance fiber optic datalinks for satellite data management](#)

 PW Marshall, CJ Dale, MA Carts, KA LaBel - Nuclear Science, IEEE Transactions on, 1994 - [ieeexplore.ieee.org](#)

... Fiber Optic Data Links for Satellite Data Management ... are demonstrated to assess particle-induced bit errors on ... Mbps and at various incident optical power levels ...

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[\[BOOK\] Dynamic Power Management: Design Techniques and CAD Tools](#)

 L Benini, G De Micheli - 1998 - [books.google.com](#)

... 6 DYNAMIC POWER MANAGEMENT ... proposed to describe the short circuit power dissipation of an inverter ... factor of a MOS transistor, VT is its threshold voltage and ...

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[A 32-bit PowerPC system-on-a-chip with support for dynamic voltage scaling and dynamic frequency ... - ►ibm.com \(PDF\)](#)

 KJ Nowka, GD Carpenter, EW MacDonald, HC Ngo, BC ... - IEEE Journal of Solid-State Circuits, 2002 - [ieeexplore.ieee.org](#)

... A 32-bit off-chip bus access can consume ... the areas of scalable and low-power system design ... monitoring, and hardware and software power-management techniques for ...

 Cited by 64 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 4 versions](#)

[System architecture directions for networked sensors - ►ucla.edu \(PDF\)](#)

 J Hill, R Szewczyk, A Woo, S Hollar, D Culler, K ... - ACM SIGPLAN Notices, 2000 - [portal.acm.org](#)

... the technology is crossing a critical threshold that makes ... It provides 32 8-bit general registers and runs at 4 ... necessary for wake up, and power save, which is ...

 Cited by 2088 - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 109 versions](#)

System for distributed power management in portable computers

K Kannan, CD Jones, NJ Lee, K Leontiadis, FP Novak ... - US Patent 5,423,045, 1995 - freepatentsonline.com
... that the charge of the battery is below a particular threshold. ... are determined by
setting the associated mask bit(s) in the power management interrupt mask ...
[Cited by 68](#) - [Related articles](#) - [Web Search](#) - [All 8 versions](#)

Mobile power management for maximum battery life in wirelesscommunication networks

JM Rulnick, N Bambos - INFOCOM'96. Fifteenth Annual Joint Conference of the IEEE ..., 1996 -
ieeexplore.ieee.org
... we also consider "constant-power with threshold" solutions, which ... 22 Optimal Power
Management Solutions It remains to ... 1, which show typical bit error rates ...
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Power aware wireless microsensor systems - [► mit.edu \(pdf\)](#)

A Chandrakasan, R Min, M Bhardwaj, S Cho, A Wang - Solid-State Circuits Conference, 2002. ESSCIRC 2002.
..., 2002 - ieeexplore.ieee.org
... be reduced to lower the energy utilized per bit. ... Hence, until advances in low-power
receiver technology ... indoor microsensor networks will not save energy using ...
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Design and optimization of dual-threshold circuits for low-voltage low-power applications

L Wei, Z Chen, K Roy, MC Johnson, Y Ye, VK De - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1999 - [ieeexplore.ieee.org](#)

... In addition to leakage **power saving**, the dynamic **power** is ... and standby leakage **power** savings for ISCAS benchmark circuits. ... was 1 V. In active **mode** (the circuit ...

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... transmitting signal to/from sleeping node identifying its existence to a network and awakening the ...

I Crayford - US Patent 5,404,544, 1995 - [freepatentsonline.com](#)

... and the MAC can be powered down to **save power**. ... When set, the **threshold** of the twisted pair receiver will be ... core 38 will of course be in its **power saving mode**. ...

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[PDF] •A trace-based approach for determining the energy consumption of a WLAN network interface

JP Ebert, B Burns, A Wolisz - European Wireless Conference, 2002 - [docenti.ing.unipi.it](#)

... SLEEP modes of the ICs have the largest **power saving** potential ... This indicates that SLEEP **mode** can **save** a considerable amount of **power** if applicable ...

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... network having a plurality of bridging nodes which transmit a beacon to terminal nodes in power ...

RL Mahany, RC Meier, RE Luse - US Patent 6,374,311, 2002 - [Google Patents](#)

... based on logical distance in view of **threshold** ... COMMUNICATION **NETWORK** HAVING A PLURALITY OF BRIDGING NODES ... A BEACON TO TERMINAL NODES IN **POWER SAVING** STATE THAT ...

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Dual-threshold voltage techniques for low-power digital circuits - •mit.edu [PDF]

JT Kao, AP Chandrakasan - Solid-State Circuits, IEEE Journal of, 2000 - [ieeexplore.ieee.org](#)

... Initially, the AND gates and the carry-**save** adder units ... that is used for gating the **power** supply rails. ... that can switch during the evaluate **mode** and utilizes ...

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System and method for providing seamless handover in a wireless computer network

WJM Diepstraten, L Monteban - EP Patent 0,851,631, 1998 - [freepatentsonline.com](#)

... present invention takes advantage of a **power saving** protocol provision ... to Step 401 and remains in the active **mode**. ... MS 50 then sends a **power save** poll (PS-POLL ...

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Power aware network interface management for streaming multimedia

D Bertozzi, L Benini, B Picco - Wireless Communications and Networking Conference, 2002. ..., 2002 - [ieeexplore.ieee.org](#)

... NIC IS OFF: **POWER SAVING** SERVER ... III. OFF - **MODE** SHORTCOMINGS The main problems that arise when trying to exploit the off- **mode** to **save power** during an ...

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A survey on sensor networks - •uftrj.br [PDF]

IF Akyildiz, W Su, Y Sankarasubramaniam, E Cayirci - Communications Magazine, IEEE, 2002 - [ieeexplore.ieee.org](#)

... Another possible **mode** of internode commu- ... to prolong **network** lifetime in a sensor **network** means that ... tion is achieved by the use of **power saving** operation ...

[Cited by 2859](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 68 versions](#)

System and method for dual **mode** DC-DC **power** conversion

DH Soo - US Patent 5,548,206, 1996 - freepatentsonline.com

... by the transistor inverter **network** even when ... has increased above a predetermined **threshold**, the converter is ... a straight line upcharge during **power saving mode**. ...

Cited by 20 - Related articles - Web Search - All 7 versions

A routing protocol and energy efficient techniques in Bluetooth scatternets - [inria.fr \[PDF\]](#)

BJ Prabhu, A Chockalingam - Communications, 2002. ICC 2002. IEEE International ..., 2002 - [ieeexplore.ieee.org](#)

... Section, we present two techniques which can **save** energy in ... The protocol used the available battery **power** in the Bluetooth ... We also proposed two energy **saving** ...

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